

## ABSTRACT OF THE DISCLOSURE

In a scan flip-flop circuit, a first master latch circuit receives usual mode data at a usual data input terminal in synchronization with a first clock signal. A second master latch circuit receives scan-in data at a scan-in data input terminal in synchronization with first and second scan clock signals. A slave latch circuit receives an output signal of the first master latch circuit in synchronization with said first clock signal and the second scan clock signal. The slave latch circuit is constructed by a control circuit for controlling transfer of the usual mode data to the output terminal in synchronization with the second scan clock signal.

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